

Notice of References Cited

Application/Control No.

09/904,383

Applicant(s)/Patent Under

Reexamination

LIN, CHUANG-CHIEH

Examiner

DuyVu n Deo

Art Unit

1765

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	C	US-6,184,104	02-2001	Tan et al.	438/401
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, 1986, Vol. 1, page 407, 429.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.